



501.38505CX2

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: N. YAMAMOTO, et al.
Serial No: 10/760,358
Filed: January 21, 2004
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD FOR MANUFACTURING THE SAME
Group: 2818
Examiner: Renee R. BERRY

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 24, 2005

Sir:

In response to the Office Action dated September 24, 2004 in the above-identified application, the Amendments, Remarks and Terminal Disclaimer as listed below and as set forth on the following pages are submitted.

Amendments to the Specification;

Amendments to the Claims;

Remarks are included as set forth below.

A Terminal Disclaimer is attached following the Remarks.